

SPECIFICATION OF LCD MODULE

CUSTOMER 客户名称	
PART NO. 产品型号	JHD754B FP/W 1.0
PRODUCTS TYPE 产品内容	
REMARKS 备注	
SIGNATURE BY CUST 客户签署:	OMER



深圳市晶汉达电子有限公司

晶汉达・JHD

LCM System





•REVISION RECORD

REV. NO.	REV. DATE	DESCRIPTION OF REVISION	PAGE	REMA RK
1.0	14/04/01	INITIAL RELEASE	ALL	
			+	



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1. FEATURES

•Display construction	128*64 DOTS
•Display mode	FSTN
•Display type	Positive Transflective
•Backlight	LED/3.1V(White)
•Viewing direction	6 o'clock
•Operating temperature	-20 to 70 °C
•Storage temperature	-30 to 80°C
•Driving voltage	Single power
•Driving method	1/65 duty, 1/9 bias
•Type	COG (Chip On GLASS)
•Drive IC	ST7565R
•Number of data line	Serial/Parallel 6800/8080
	Series MPU interface
•Connector	FPC

2. MECHANICAL DATA

-	ГЕМ	WIDTH	HEIGHT	THICKNE SS	UNIT
	Module Size Include component of FPC)		36.8+18.8	6.0 ± 0.3	mm
Resc	Resolution		128×64		dots
Acti	ve area	44.7	23.01 –		mm
Viewi	ng area	51.0	26.0	_	
Det	Size	0.32	0.33	_	mm
Dot	Pitch	0.35	0.36	_	mm
Diameter of	Diameter of mounting hole		-		mm

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3. ABSOLUTE MAXIMUM RATINGS

Cperation Votage	Symbol	Ratings
Operation Voltage	VDD	0.3V~3.6V
Drive Supply Voltage	VLCD	0.3V~14.5V
Input Voltage Range	VIN	-0.3V~VDD+0.3V
Operation Temperature	TOPR	0℃~50 ℃
Storage Temperature	TSTR	-10 ℃~60 ℃
Humidity		90%RH

4. ELECTRICAL CHARACTERISTICS

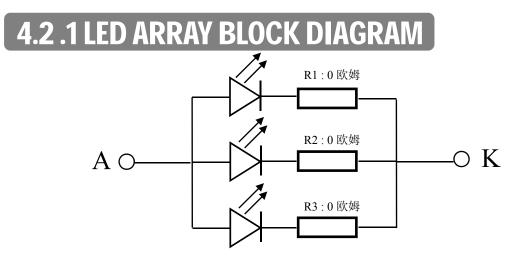
Item	Item		Condition	Min.	Тур.	Max.	Unit
Supply Voltage	Logic	V _{dd}			3.0		V
	H level	VIH		$0.8V_{DD}$		V _{DD}	v
Input Voltage	L level	VIL		V_{ss}		$0.2V_{\text{DD}}$	V
Current Consumption		I _{DD}	With internal V _{⊾∞} generation; V∞=3.0V;Ta=25℃; 4x charge pump			250	uA
LCD Driving V	oltage	V _{LCD}	Bias=1/9 VLCD=V0-Vss	8.8	9.0	9.2	v

4.1 LED ELECTRICAL/OPTLCAL CHARACTERISTICS

Item	Symbol	Min.	Тур.	Max.	Unit	Condition
Forward Voltage	Vf	2.7	3.1	3.4	V	If= 45mA
Reverse Current	Ir	_	50	—	uA	Vr=5 V
Dominant wave length	λp	_	X=0.29 Y=0.30	_	nm	If=45mA
Spectral Line Half width	Δλ	_	—	—	_	If=mA
Luminance	Lv	80	100	_	cd/m ²	If=45mA

4.2 LED ABSOLUTE MAXIMUM RATINGS

Item	Symbol	Condition	Rating	Unit
Reverse Voltage	Vr	Ta=25℃	5	V
Absolute maximum forward current	Ifm	Ta=25℃	75	mA
Power description	pd	Ta=25℃	375	mW

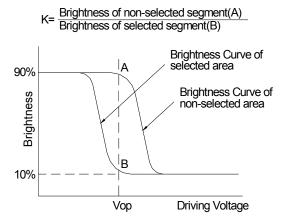




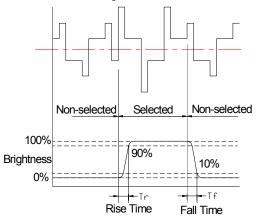
5. ELECTRO-OPTICAL CHARACTERISTICS

ITEM	SYMBO L	CONDITIO N	MIN.	TYP.	MAX.	UNIT	NOTE
Contrast ratio	К	φ=0	1.4	4	-	-	1
Response time (rise)	Tr	φ=0	-	250	300	ms	2
Response time (fall)	Tf	φ=0	-	250	350	ms	2
	φ	K >2.0	-40 +40)	dog	3
Viewing angle	θ	K ≥2.0	-45 +15			deg.	ა

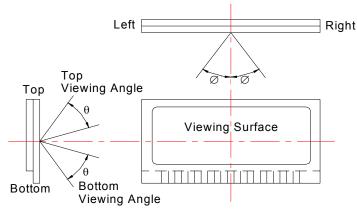
Note 1: Definition of Contrast Ratio "K"



Note 2: Definition of Optical Response Time



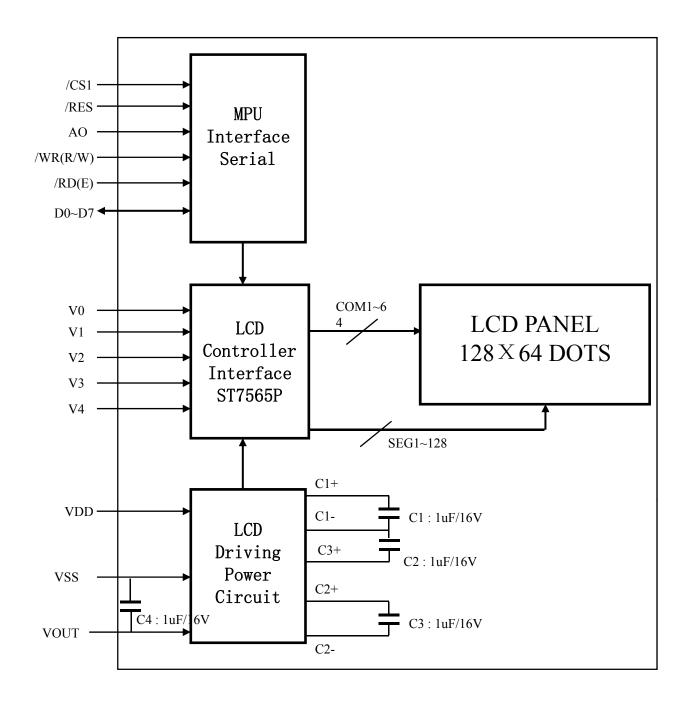
Note 3: Definition of Viewing Angle



Please select either top or bottom viewing angle

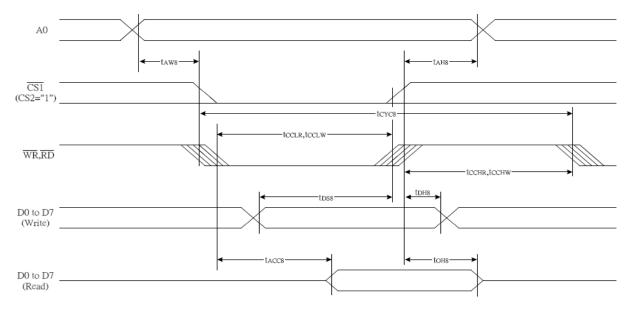


6. BLOCK DIAGRAM

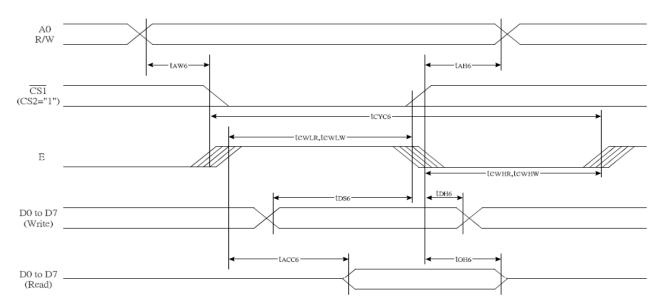


7. TIMING DIAGRAM

System Bus Read/Write Characteristics 1 (For the 8080 Series MPU)



	-			(VDD = 3.3V,		<u>o 85°C)</u>
Item	Signal	Symbol	Condition	Rat Min.	Max.	Units
Address hold time		tAH8		0		
Address setup time	A0	tAW8		0	_	-
System cycle time		tcyc8		240	_	1
Enable L pulse width (WRITE)		tCCLW		80	_	
Enable H pulse width (WRITE)	WR	tcchw		80	_	1
Enable L pulse width (READ)	RD	tCCLR		140	_	Ns
Enable H pulse width (READ)		tCCHR		80		
WRITE Data setup time		tDS8		40	_	
WRITE Address hold time	D0 to D7	tDH8		0	_	
READ access time		tACC8	C∟ = 100 pF	_	70	
READ Output disable time		toh8	C∟ = 100 pF	5	50	
Address hold time		tAH8		0	- 1	1
Address setup time	A0	tAW8		0	_	1
System cycle time		tcyc8		400	_	7
Enable L pulse width (WRITE)	WR	tooi w		220	_	1
Enable H pulse width (WRITE)	- VVR	tcchw		180	_	1
Enable L pulse width (READ)	RD	tCCLR		220	_	ns
Enable H pulse width (READ)		tCCHR		180	_	1
WRITE Data setup time		tDS8		40	_	1
WRITE Address hold time	D0 to D7	tdh8		0	_	1
READ access time	001007	tACC8	CL = 100 pF	_	140	1
READ Output disable time	7	toh8	C∟ = 100 pF	10	100	7

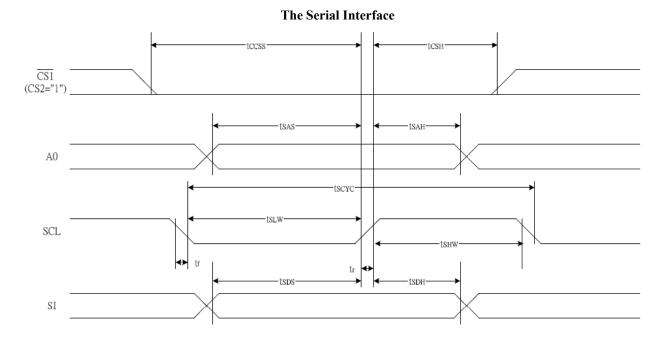


System Bus Read/Write Characteristics 2 (For the 6800 Series MPU)

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				(VDD = 3.3V, Rat		(
Item	Signal	Symbol	Condition	Min.	Max.	Units
Address hold time		t AH6		0	_	
Address setup time	A0	t AW6		0	_	
System cycle time		tcyc6		240	_	
Enable L pulse width (WRITE)	WR	tewlw		80	_	
Enable H pulse width (WRITE)	VVR	tewnw		80		
Enable L pulse width (READ)	RD	tewlr		80	_	ns
Enable H pulse width (READ)		tewhr		140		
WRITE Data setup time		tDS6		40		
WRITE Address hold time	D0 to D7	tDH6		0	_	
READ access time	001007	tACC6	C∟= 100 pF	-	70	
READ Output disable time	_	toh6	C∟= 100 pF	5	50	
Address hold time		tAH6		0	_	
Address setup time	AO	tAW6		0	_	
System cycle time	_	tcyc6		400	_	
Enable L pulse width (WRITE)	WR	tewlw		220	_	
Enable H pulse width (WRITE)	VV K	tewnw		180	_	
Enable L pulse width (READ)	RD	tEWLR		220	_	ns
Enable H pulse width (READ)	KU	tewhr		180	_	
WRITE Data setup time		tDS6		40	_	
WRITE Address hold time	D0 to D7	tDH6		0	_	7
READ access time		tACC6	C∟= 100 pF	_	140	7
READ Output disable time	1	tOH6	C∟ = 100 pF	10	100	7





				(VDD = 3.3V,	Ta = -30 to	o 85°C)
Item	Signal	Symbol	Condition	Rat	Units	
Reni	Signal		Condition	Min.	Max.	Cinto
Serial Clock Period		Tscyc		50	_	
SCL "H" pulse width	SCL	Tshw		25	_	
SCL "L" pulse width		Tslw		25	_	
Address setup time	A0	TSAS		20	_	
Address hold time	A0	⊤sah		10	_	ns
Data setup time	SI	Tsds		20	_	
Data hold time	- 51	Tsdh		10	_	1
CS-SCL time	CS	Tcss		20	_	1
CS-SCL time		Tcsh		40	_	1

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8. INSTRUCTION SET

Command	Command Code							Function				
Command	A0	/RD	/WR					D3				
(1) Display ON/OFF	0	1	0	1	0	1	0	1	1	1	0 1	LCD display ON/OFF 0: OFF, 1: ON
(2) Display start line set	0	1	0	0	1	Di	spla	ıy sta	art a	ddre	ss	Sets the display RAM display start line address
(3) Page address set	0	1	0	1	0	1	1	Pa	ge a	addre	ess	Sets the display RAM page address
(4) Column address set	0	1	0	0	0	0	1				cant ress	Sets the most significant 4 bits of
upper bit Column address set lower bit	0	1	0	0	0	0	0	Lea	ist s	ignif	ress cant ress	the display RAM column address. Sets the least significant 4 bits of the display RAM column address.
(5) Status read	0	0	1		St	atus		0	0	0	0	Reads the status data
(6) Display data write	1	1	0			I	Nrite	e da	ta			Writes to the display RAM
(7) Display data read	1	0	1			ł	Read	d da	ta			Reads from the display RAM
(8) ADC select	0	1	0	1	0	1	0	0	0	0	0 1	Sets the display RAM address SEG output correspondence 0: normal, 1: reverse
(9) Display normal/ reverse	0	1	0	1	0	1	0	0	1	1	0 1	Sets the LCD display normal/ reverse 0: normal, 1: reverse
(10) Display all points ON/OFF	0	1	0	1	0	1	0	0	1	0	0 1	Display all points 0: normal display 1: all points ON
(11) LCD bias set	0	1	0	1	0	1	0	0	0	1	0 1	Sets the LCD drive voltage bias ratio 0: 1/9 bias, 1: 1/7 bias (ST7565P)
(12) Read/modify/write	0	1	0	1	1	1	0	0	0	0	D	Column address increment At write: +1 At read: 0
(13) End	0	1	0	1	1	1	0	1	1	1	D	Clear read/modify/write
(14) Reset	0	1	0	1	1	1	0	0	0	1	D	Internal reset
(15) Common output mode select	0	1	0	1	1	0	0	0 1	*	*	*	Select COM output scan direction 0: normal direction 1: reverse direction
(16) Power control set	0	1	0	0	0	1	0	1		berat	ing	Select internal power supply operating mode
(17) Vo voltage regulator internal resistor ratio set	0	1	0	0	0	1	0	0		esiste atio	or	Select internal resistor ratio(Rb/Ra) mode
(18) Electronic volume mode set Electronic volume register set	0	1	0	1 0	0 0	-	0 ctroi	-	_	0 ne v	1 alue	Set the Vo output voltage electronic volume register
(19) Static indicator ON/OFF	0	1	0	1	0	1	0	1	1	0	0 1	0: OFF, 1: ON
Static indicator register set			-	0	0	0	0	0	0	0	•	Set the flashing mode
(20) Booster ratio set	0	1	0	1 0	1 0	1 0	1 0	1 0	0 0	ste	0 o-up lue	select booster ratio 00: 2x,3x,4x 01: 5x 11: 6x
(21) Power saver												Display OFF and display all points ON compound command
(22) NOP	0	1	0	1	1	1	0	0	0	1	1	Command for non-operation
(23) Test	0	1	0	1	1	1	1	*	*	*	*	Command for IC test. Do not use this command

MODEL: JHD754B

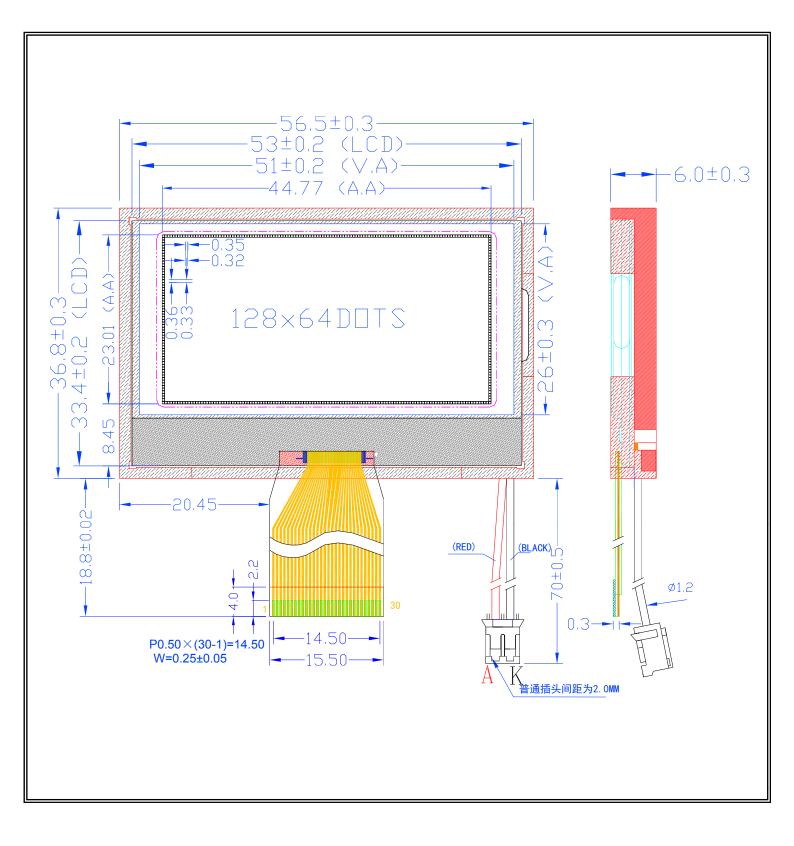
9. INSTRUCTION SEQUENCE

I NIT:

LCALL DEL_201 CLR RESET LCALL DEL_201 SETB RESET LCALL DEL_201 MOV A,#81H LCALL WC1 MOV A,BIAS LCALL WC1	MS
;MOV A,#84H ;LCALL WC1 ;MOV A,#07H ;LCALL WC1 ;MOV A,#82H ;LCALL WC1 ;MOV A,#05H ;LCALL WC1	
MOV A,#0A0H	;ADC
LCALL WC1 MOV A,#0C8H LCALL WC1	;SHL
MOV A,#0A3H LCALL WC1 MOV A,#2FH LCALL WC1 MOV A,#25H LCALL WC1 MOV A,#040H LCALL WC1 MOV A,#0AFH LCALL WC1 RET	;COM0

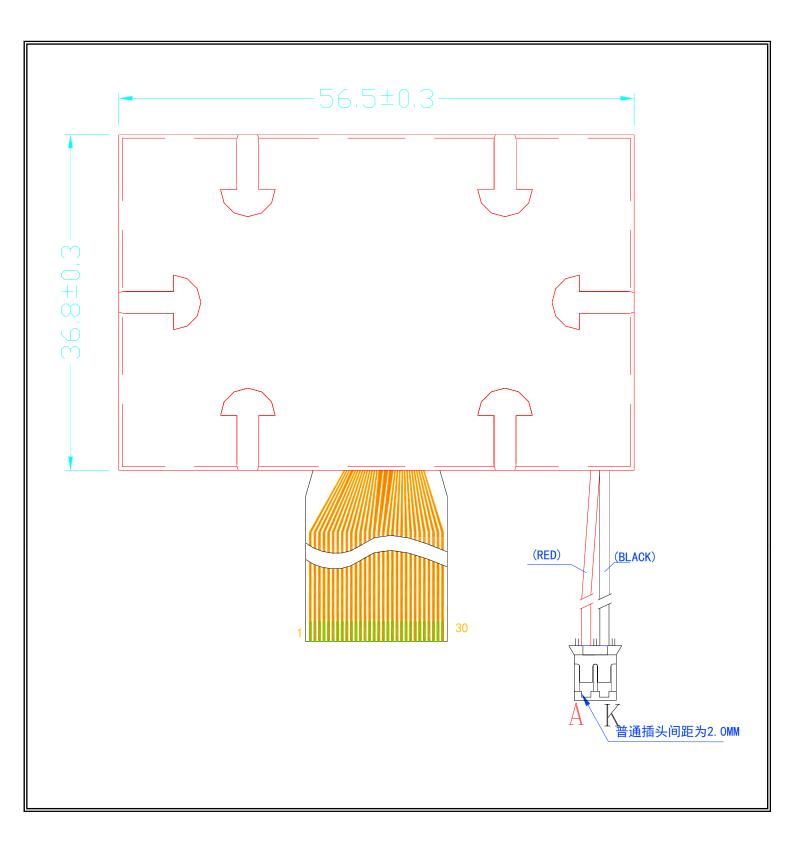


10. OUTLINE DRAWING



MODEL: JHD754B

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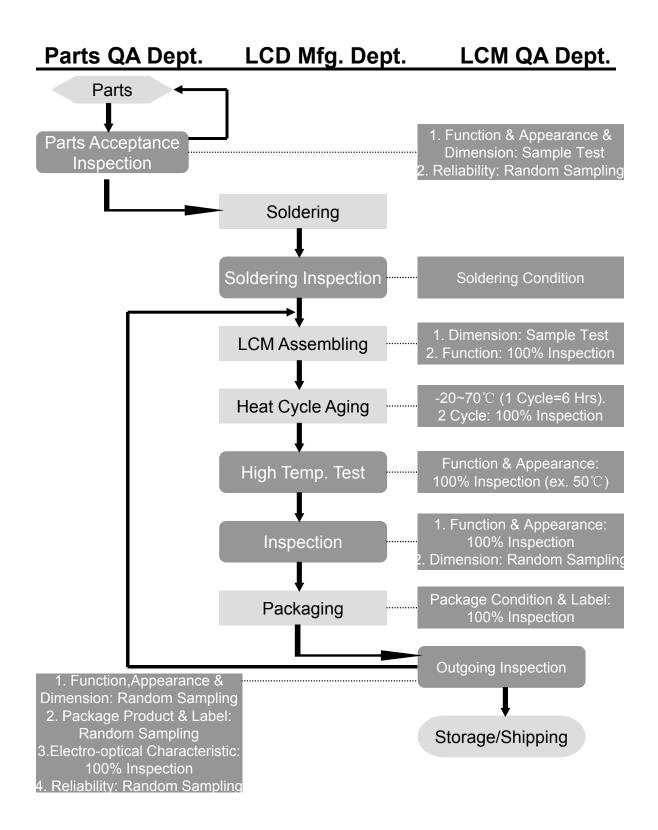




11.INTERFACE

PIN	SYMBOL	I/O	LEVEL	INSTRUCTION	
1	INTRS	I	H/L	This terminal selects the resistors for the V0 voltage level adjustment. IRS = "H": Use the internal resistors IRS = "L": Do not use the internal resistors.	
2	PS	I	H/L	This pin configures the interface to be parallel mode or serial mode. P/S = "H": Parallel data input/output. P/S = "L": Serial data input.	
3	C86	I	H/L	This is the MPU interface selection pin. C86 = "H": 6800 Series MPU interface. C86 = "L": 8080 Series MPU interface.	
4	VR	I	-	Output voltage regulator terminal. Provides the voltage between VSS and V0 through a resistive voltage divider. IRS = "L" : the V0 voltage regulator internal resistors are not used IRS = "H" : the V0 voltage regulator internal resistors are used.	
5~9	V0~V4	I/O	-	LCD drive supplay voltage. The voltage determined by LCD pixel is impedance converted by an operational amplifier for application. Voltage should have the following relationship: V0≥V1 ≥V2 ≥V3≥V4 ≥VSS.	
10	C2-	0	-	Capacitor 2 negative Connection pin for voltage converter.	
11	C2+	0	-	Capacitor 2 positive Connection pin for voltage converter.	
12	C1+	0	-	Capacitor 1 positive Connection pin for voltage converter.	
13	C1-	0	-	Capacitor 1 negative Connection pin for voltage converter.	
14	C3+	0	-	Capacitor 3 positive Connection pin for voltage converter.	
15	VOUT	I/O	-	Voltage converter input/output pin.	
16	VSS	SUPPLY	L	Ground	
17	VDD	SUPPLY	Н	Power Supply Voltage	
18~ 25	D7(SI) D6(SCL) D5~D0	I/O	H/L	This is an 8-bit bi-directional data bus that connects to an 8-bit of 16bit standard MPU data bus.	
26	/RD (E)	I	H/L	8080:Read execution control pin. 6800:enable clock input control pin.	
27	/WR (R/W)	I	H/L	8080:Write execution control pin. 6800:When R/W = "H": Read. When R/W = "L": Write.	
28	A0	I	H/L	Register select input pin.	
29	/RES	I	L	Reset select input pin,low effective.	
30	/CS1	I	L	Chip select input pin, low effective.	

12. QC/QA PROCEDURE



13. RELIABILITY

•Operating life time: Longer than 50000 hours (at room temperature without direct irradiation of sunlight)

•Reliability Characteristics:

Item	Test	Criterion		
High temp	70°C / 200 Hrs	Total current consumption should be below double of initial value		
Low temp.	-20°C / 200 Hrs			
High humidity	40°C * 90%RH / 200 Hrs	Contrast ratio should be within initial		
Thermal shock	-20°C→25°C→70°C→25°C /5 Cycles (30min) (5min) (30min) (5min)	value±50% ■No defect in cosmetic and operational		
Vibration	 1.Operating time: Thirty minutes exposure in each direction (x, y, z) 2.Sweep Frequency (1min):10Hz→ 55Hz →10Hz 3.Amplitude: 0.75mm double amplitude 	function is allowable		



Handling Precaution

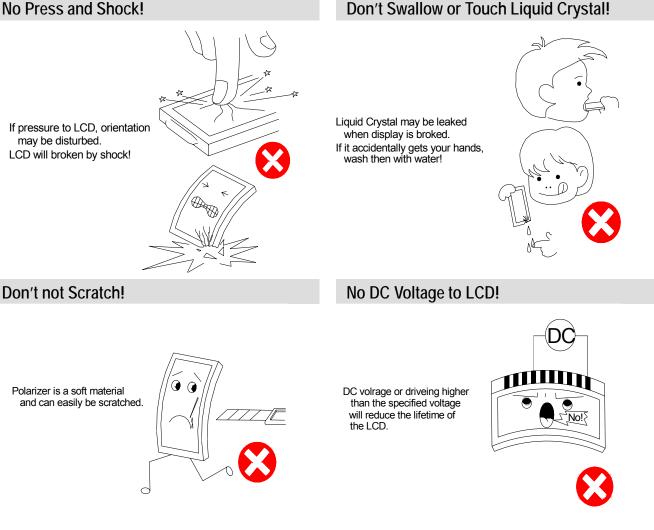
1. Limitation of Application:

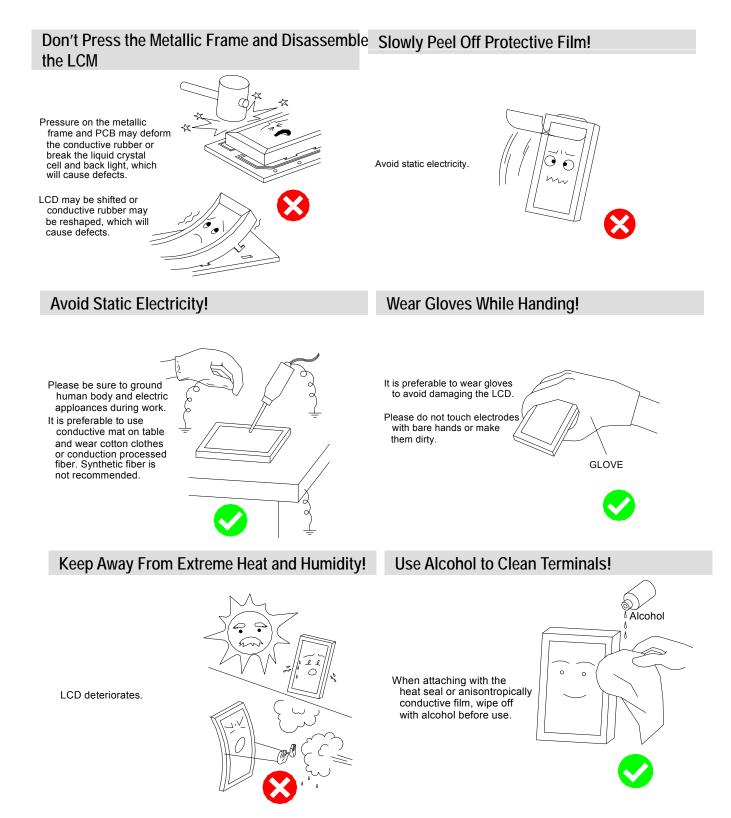
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No Press and Shock!





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Don't Drop Water on LCD!

Note that the presence of waterdrops or dew in the LCD panel may deteriorate the polarizer or corrade electrode.



Precaution in Soldering LCD Module

Basic instructions: Solder I/O terminals only. Use soldering iron without leakage.

(1)Soldering condition to I/O terminals

Temperature at tip of the iron: $280 \pm 10^{\circ}$ C

Soldering time: 3~4 sec.

Type of solder: Eutectic solder (containing colophony-flux)

*Please do not use flux because it may soak into LCD Module or contaminate it.

*It is preferable to peel off protective film on display surface after soldering I/O terminals is finished.

(2)Remove connector or cable

*When you remove connector or cable soldered to I/O terminals, please confirm that solder is fully melted. If you remove by force, electrodes at I/O terminals may be damaged(or stripped off).

*It is recommended to use solder suction machine.

Long-term Storage

If it is necessary to store LCD modules for a long time, please comply with the following procedures.

If storage condition is not satisfactory, display(especially polarizer) may be deteriorated or soldering I/O terminals may become difficult(some oxide is generated at I/O terminals plating).

1.Store as delivered by Jing handa

2.If you store as unpacked,put in anti-static bag,seal its opening and store where it is not subjected to direct sunshine nor fluorescent lamp.

3.Store at temperature 0 to +35 ℃ and at low humidity.Please refer to our specification sheets for storage temperature range and humidity condition.

Long-term Storage

Please use power supply with built-in surge protection circuit.